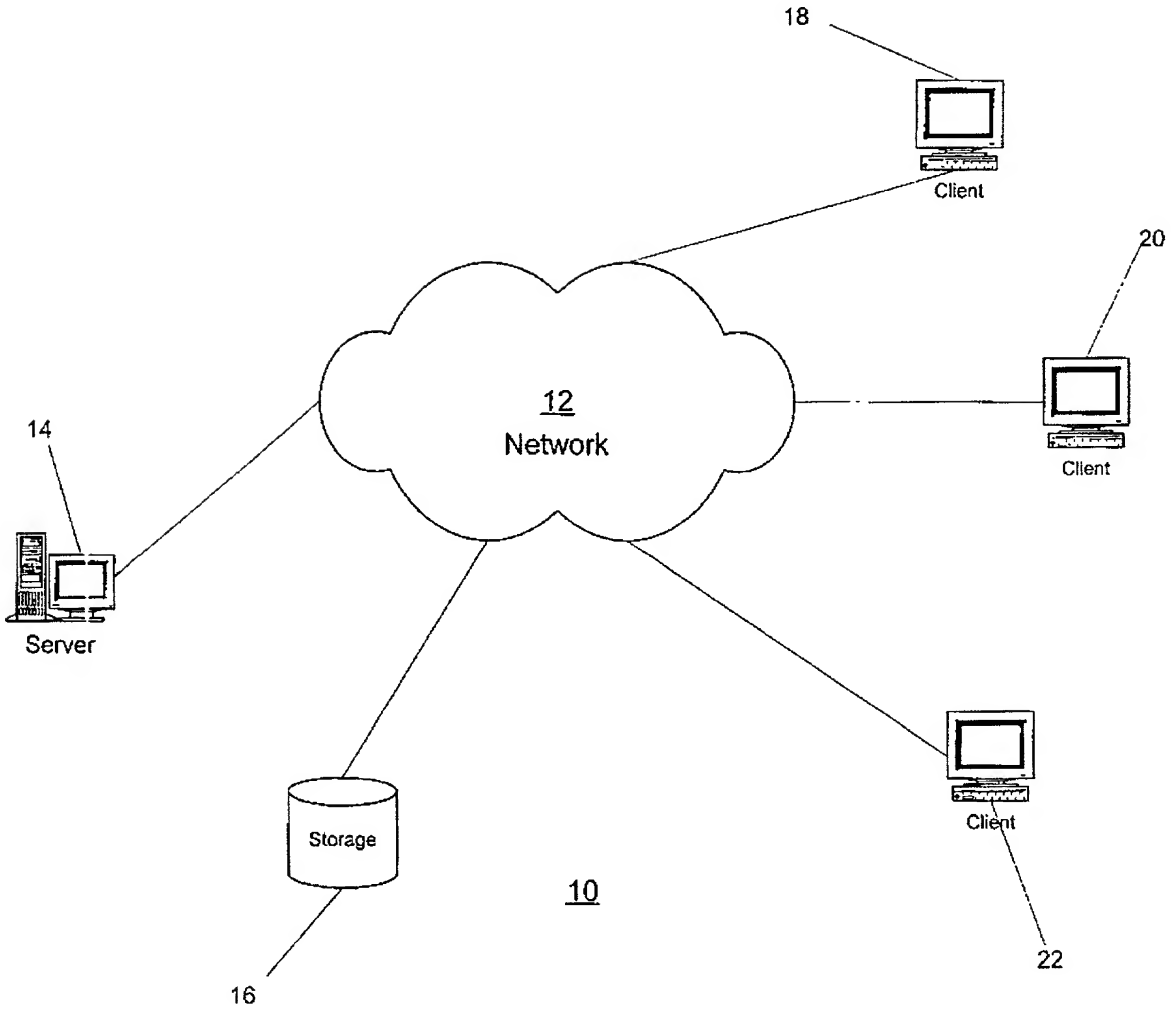
ET420197489US

Figure 1



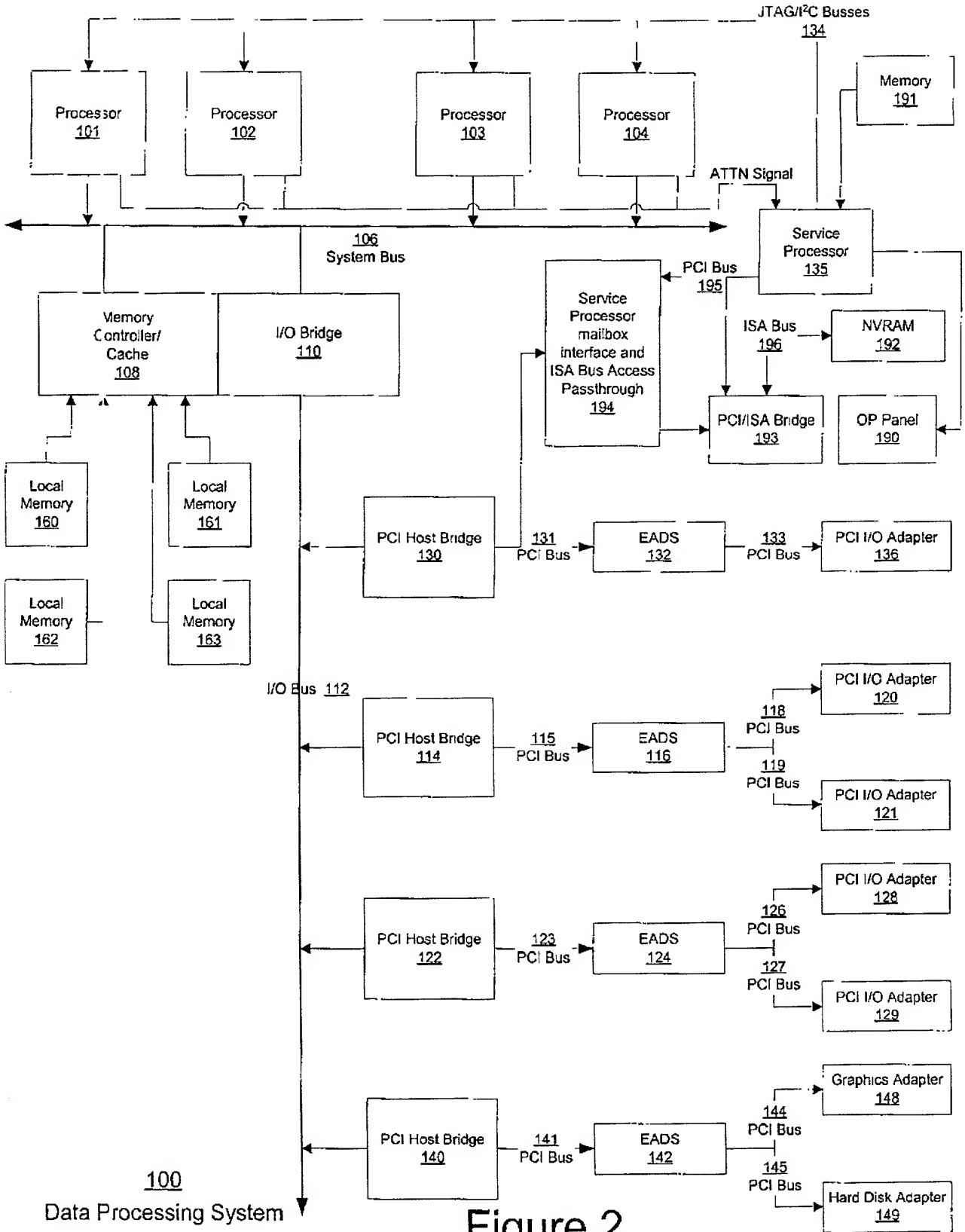
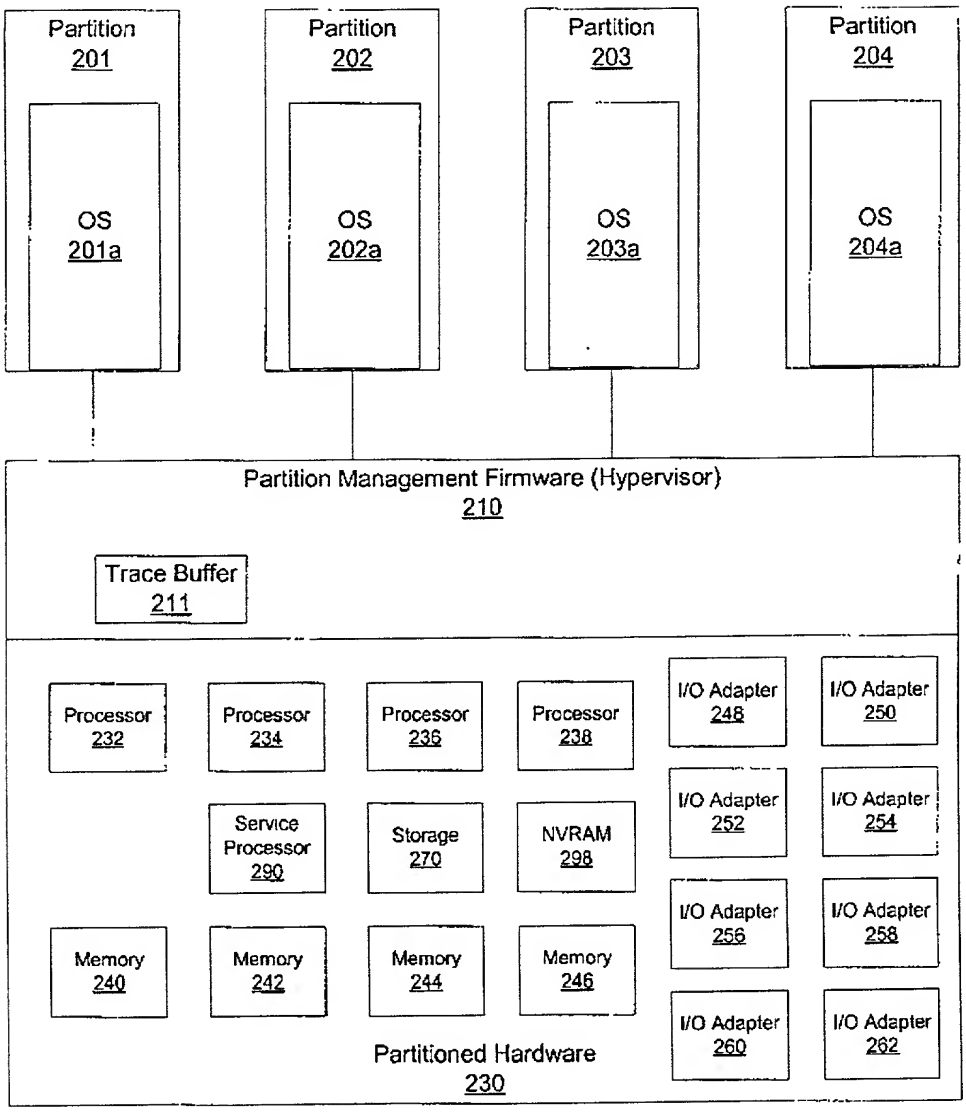

JTAG/I²C Busses
134
Processor
101
Processor
102
Processor
103
Processor
104
Memory
191
ATTN Signal
106
System Bus
Service
Processor
135
PCI Bus
195
Service
Processor
mailbox
interface and
ISA Bus Access
Passthrough
194
ISA Bus
196
NVRAM
192
Memory
Controller/
Cache
108
I/O Bridge
110
PCI/ISA Bridge
193
OP Panel
190
Local
Memory
160
Local
Memory
161
Local
Memory
162
Local
Memory
163
PCI Host Bridge
130
131
PCI Bus
EADS
132
133
PCI Bus
PCI I/O Adapter
136
I/O Bus 112
PCI Host Bridge
114
115
PCI Bus
EADS
116
118
PCI Bus
119
PCI Bus
PCI I/O Adapter
120
PCI I/O Adapter
121
PCI Host Bridge
122
123
PCI Bus
EADS
124
126
PCI Bus
127
PCI Bus
PCI I/O Adapter
128
PCI I/O Adapter
129
PCI Host Bridge
140
141
PCI Bus
EADS
142
144
PCI Bus
145
PCI Bus
Graphics Adapter
148
Hard Disk Adapter
149
Hard Disk
150
100
Data Processing System

Figure 2



200
Logically Partitioned Platform

Figure 3

Docket No. AUS920110007US1

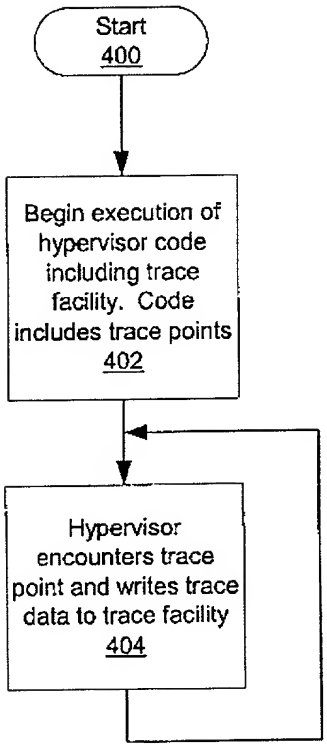


Figure 4

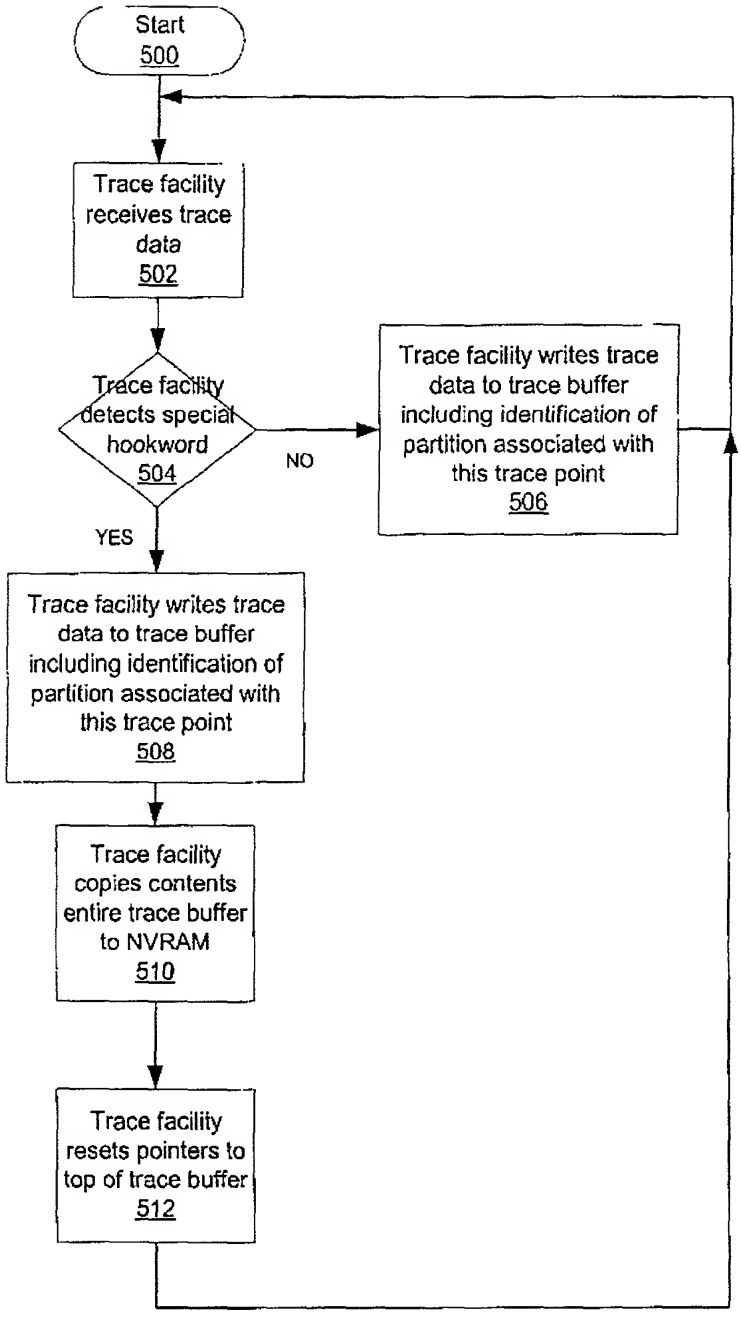

Start
500
Trace facility receives trace data
502
Trace facility detects special hookword
504
NO
YES
Trace facility writes trace data to trace buffer including identification of partition associated with this trace point
506
Trace facility writes trace data to trace buffer including identification of partition associated with this trace point
508
Trace facility copies contents entire trace buffer to NVRAM
510
Trace facility resets pointers to top of trace buffer
512

Figure 5

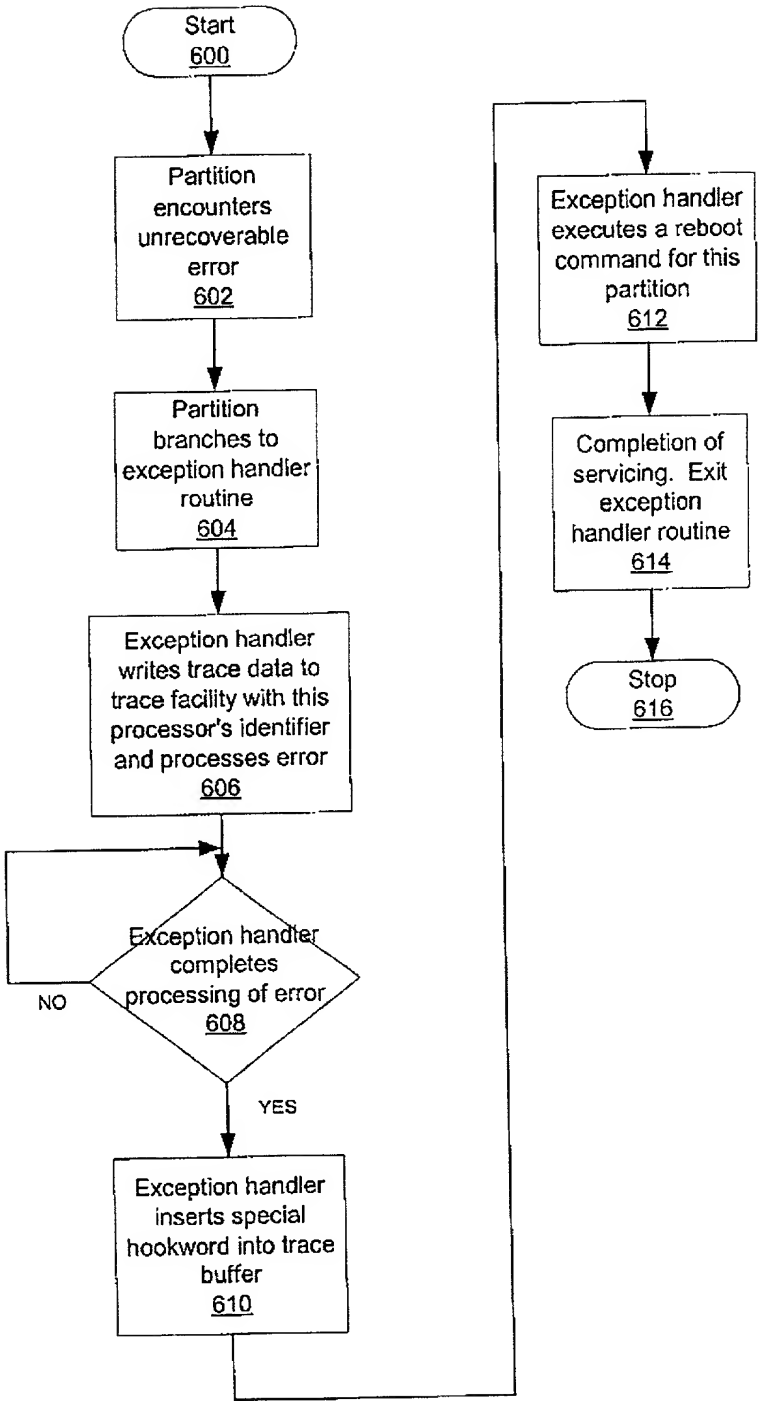

Start
600
Partition encounters unrecoverable error
602
Partition branches to exception handler routine
604
Exception handler writes trace data to trace facility with this processor's identifier and processes error
606
Exception handler completes processing of error
608
NO
YES
Exception handler inserts special hookword into trace buffer
610
Exception handler executes a reboot command for this partition
612
Completion of servicing. Exit exception handler routine
614
Stop
616

Figure 6